TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

2SK3068

Chopper Regulator DC–DC Converter, and Motor Drive Applications

- Low drain-source ON resistance $: RDS(ON) = 0.4 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 9.0 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode $: V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	500	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	12	А	
	Pulse (Note 1)	I _{DP}	48	А	
Drain power dissipation	n (Tc = 25°C)	PD	100	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	364	mJ	
Avalanche current		I _{AR}	12	А	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	10	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

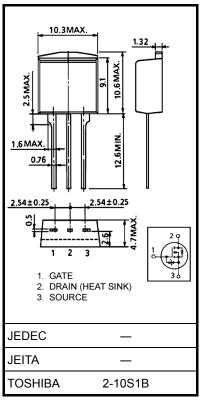
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

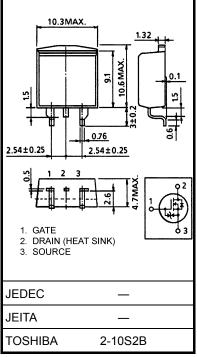
Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.3 mH, R_G = 25 Ω , I_{AR} = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 1.5 g (typ.)



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Unit: mm

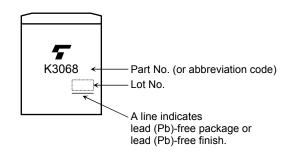
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V		—	±10	μA
Gate-source br	eakdown voltage	V _(BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	-	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 6 A	-	0.4	0.52	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	4.0	9.0	_	S
Input capacitance	ce	C _{iss}		_	2040	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	200	_	pF
Output capacitance		C _{oss}			640	_	
Switching time	Rise time	tr	V_{GS}^{10V} $I_{D}=6A$ V_{OUT} V_{OUT} R_{L} $=33\Omega$ $V_{DD}=200V$	_	22	_	
	Turn-on time	t _{on}		_	58	_	20
	Fall time	t _f		_	36	_	- ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10µs		180		
Total gate charge (Gate-source plus gate-drain)		Qg		_	45	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 10 A -		25	—	nC
Gate-drain ("miller") charge		Q _{gd}			20	_	

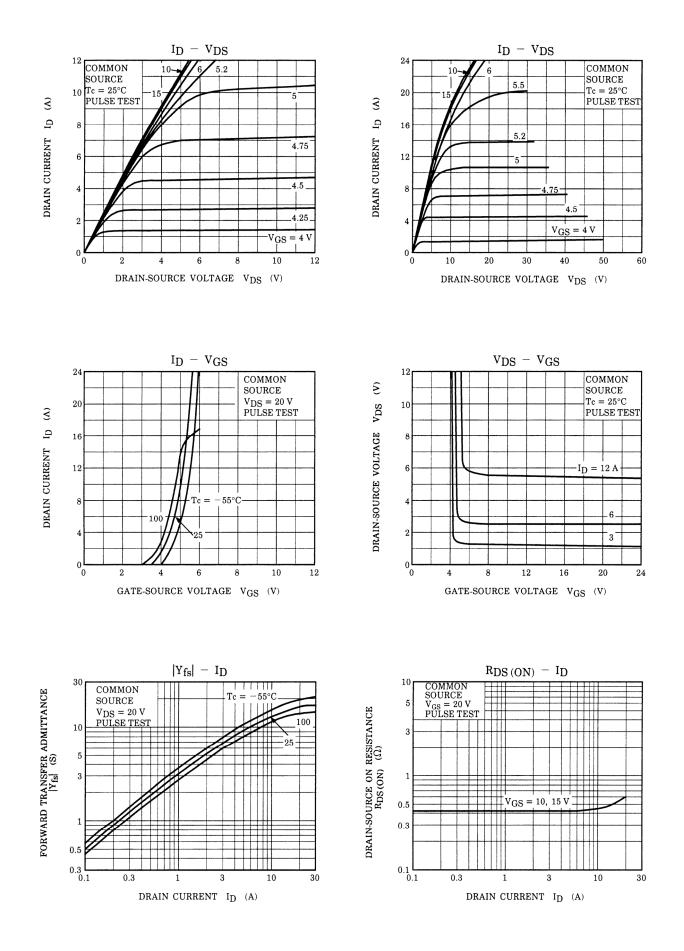
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	12	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	48	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V		370	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / µs		3.5	_	μC

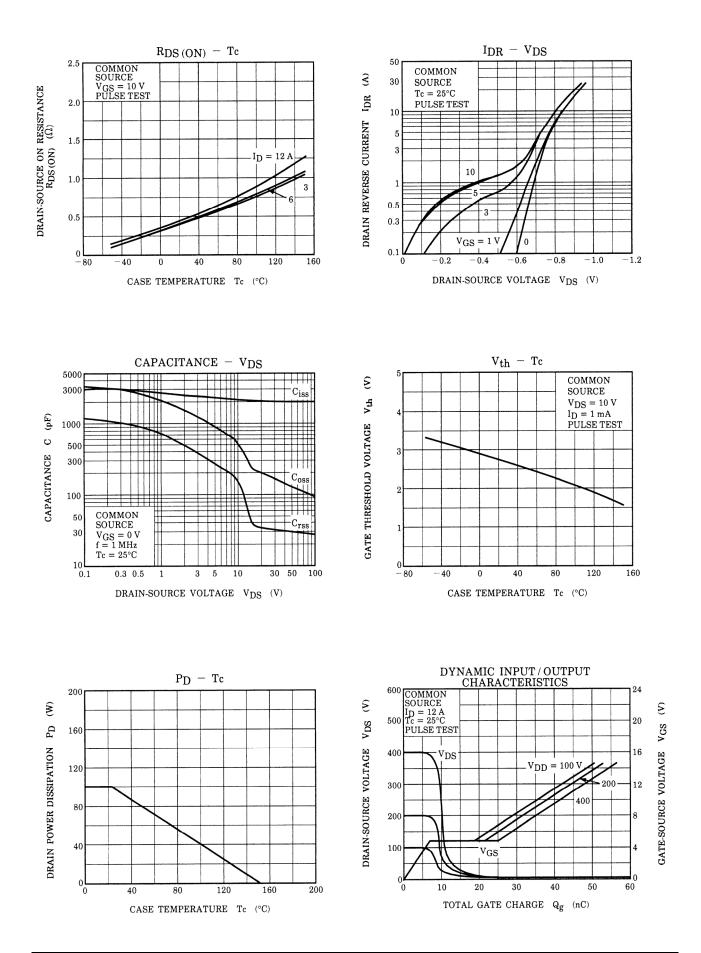
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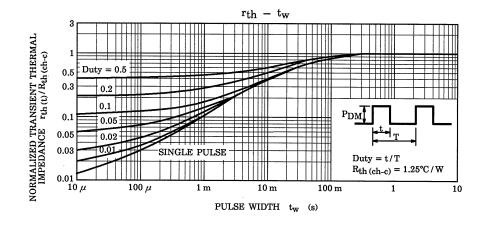
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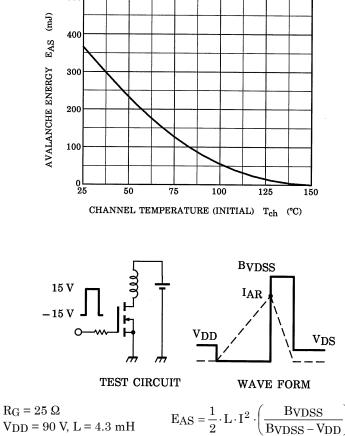
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SAFE OPERATING AREA 100 500 I_D MAX. (PULSED) \otimes (ful) 50 30 $\mathbf{E}_{\mathbf{AS}}$ 100 μ s%- ID MAX. (CONTINUOUS) 1111 (¥) AVALANCHE ENERGY 10 DRAIN CURRENT ID 1 ms* 5 3 DC OPERATION Tc = 25° C 0.5 0.3 SINGLE * NONREPETITIVE PULSE Tc = 25°C 0.1 Curves must be derated linearly with increase in 0.05 0.03 temperature. VDSS MAX +++10 3 30 300 1000 1 100 DRAIN-SOURCE VOLTAGE V_{DS} (V)



 $E_{AS} - T_{ch}$

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